2SJ517

Silicon P Channel MOS FET High Speed Power Switching

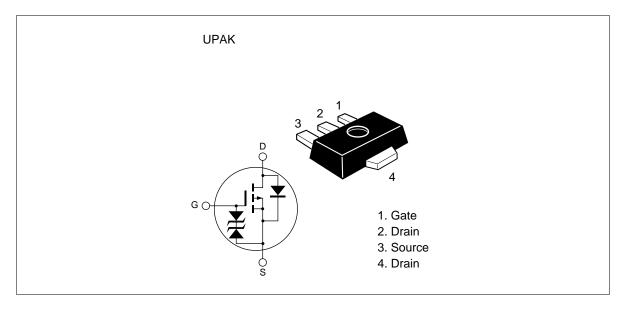
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ADE-208-575B (Z) 3rd. Edition Jun 1998

Features

- Low on-resistance $R_{DS(on)} = 0.18 \ \Omega \ typ. \ (at \ V_{GS} = -4V, \ I_D = -1A)$
- Low drive current
- · High speed switching
- 2.5V gate drive devices.

Outline





2SJ517

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	-20	V
Gate to source voltage	V_{GSS}	±10	V
Drain current	I _D	-2	A
Drain peak current	Note1 D(pulse)	-4	A
Body-drain diode reverse drain current	I _{DR}	-2	A
Channel dissipation	Pch Note2	1	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: 1. PW \leq 100 μ s, duty cycle \leq 10 %

2. When using aluminium ceramic board (12.5 x $20 \times 0.7 \text{ mm}$)

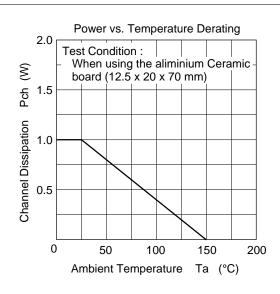
Electrical Characteristics $(Ta = 25^{\circ}C)$

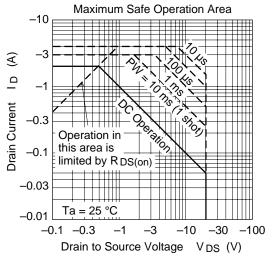
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	-20	_	_	V	$I_{D} = -10 \text{mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	±10	_	_	V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	_	_	-10	μΑ	$V_{DS} = -20 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 8V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-0.5	_	-1.5	V	$I_{D} = -1 \text{mA}, V_{DS} = -10 \text{V}$
Static drain to source on state resistance	R _{DS(on)}	_	0.18	0.24	Ω	$I_D = -1A, V_{GS} = -4V^{Note3}$
Static drain to source on state resistance	R _{DS(on)}	_	0.27	0.43	Ω	$I_D = -1A, V_{GS} = -2.5V^{Note3}$
Forward transfer admittance	y _{fs}	1.8	3.0	_	S	$I_{\rm D} = -1A, \ V_{\rm DS} = -10V^{\rm Note3}$
Input capacitance	Ciss	_	320	_	pF	V _{DS} = -10V
Output capacitance	Coss	_	190	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	90	_	pF	f = 1MHz
Turn-on delay time	t _{d(on)}	_	14	_	ns	$I_D = -1A$, $R_L = 10\Omega$
Rise time	t _r	_	75	_	ns	$V_{GS} = -4V$
Turn-off delay time	t _{d(off)}	_	90	_	ns	_
Fall time	t _f	_	90	_	ns	
Body-drain diode forward voltage	V_{DF}	_	-0.95	_	V	$I_F = -2A, V_{GS} = 0$
Body–drain diode reverse recovery time	t _{rr}	_	70	_	ns	$I_F = -2A, V_{GS} = 0$ diF/ dt =50A/ μ s

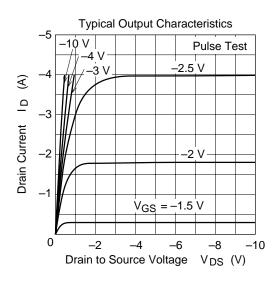
Note: 3. Pulse test

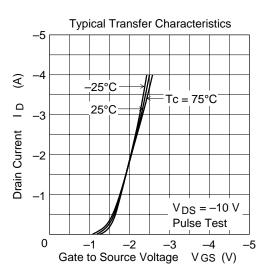
4. Marking is "YY".

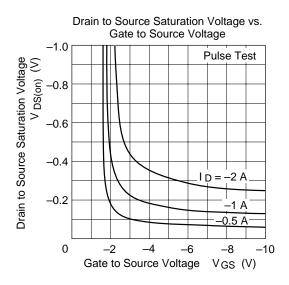
Main Characteristics

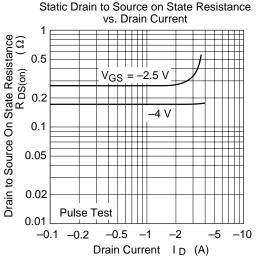


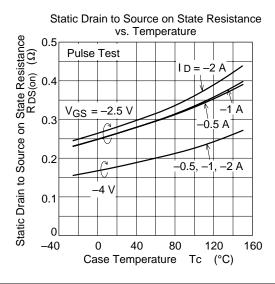


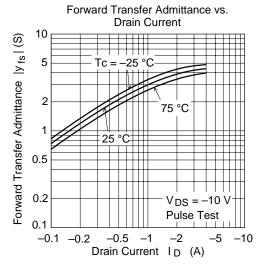


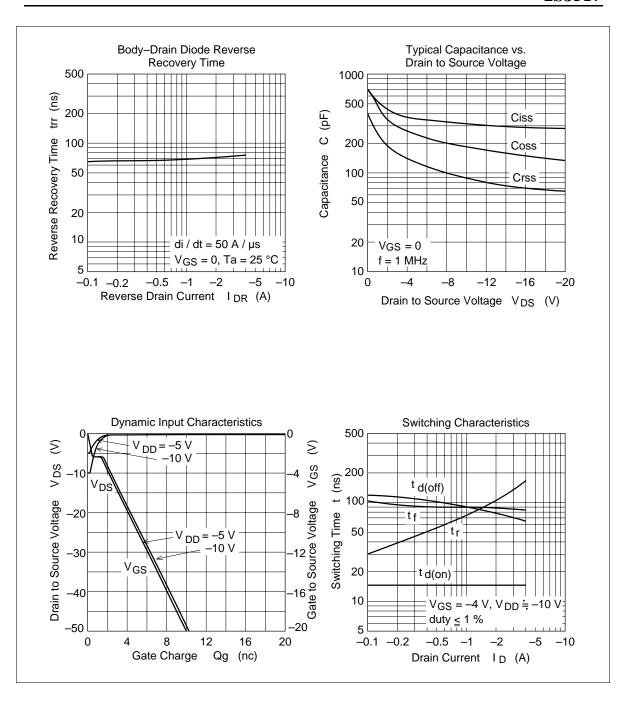


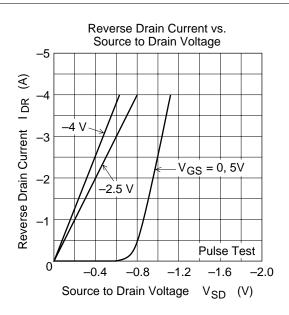




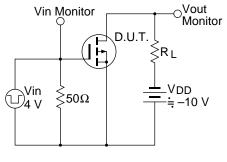




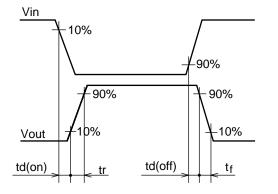




Switching Time Test Circuit

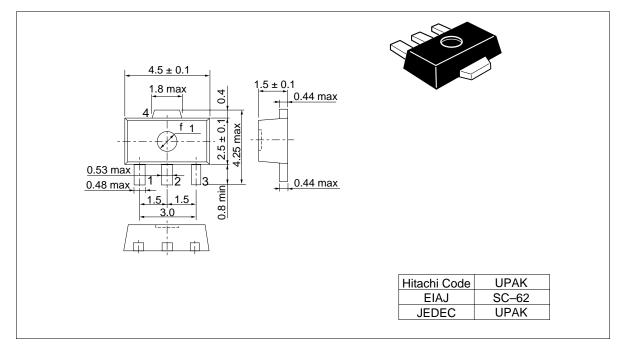


Waveform



Package Dimensions

Unit: mm



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